

# Freeform Search

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<b>Database:</b> <div style="background-color: black; color: white; padding: 2px 0;"> <a href="#">US Pre-Grant Publication Full-Text Database</a>  <a href="#">US Patents Full-Text Database</a>  <b><a href="#">US OCR Full-Text Database</a></b>  <a href="#">EPO Abstracts Database</a>  <a href="#">JPO Abstracts Database</a>  <a href="#">Derwent World Patents Index</a>  <a href="#">IBM Technical Disclosure Bulletins</a> </div>
<b>Term:</b> <div style="border: 1px solid black; padding: 2px; width: 100%;"> L35 and l34 and SDRAM </div>
<b>Display:</b> <div style="display: flex; align-items: center;"> <input type="text" value="10"/> <b>Documents in Display Format:</b> <div style="border: 1px solid black; padding: 0 2px; margin: 0 2px;"></div> <input type="text" value="1"/> <b>Starting with Number:</b> <div style="border: 1px solid black; padding: 0 2px; margin: 0 2px;"></div> </div>
<b>Generate:</b> <div style="display: flex; align-items: center;"> <input type="radio"/> <b>Hit List</b> <input checked="" type="radio"/> <b>Hit Count</b> <input type="radio"/> <b>Side by Side</b> <input type="radio"/> <b>Image</b> </div>

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## Search History

**DATE:** Sunday, February 19, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u>	<u>Hit</u>	<u>Set</u>
<u>Name</u>	<u>Count</u>	<u>Name</u>
side by side		result set
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L36</u> L35 and l34 and SDRAM	125	<u>L36</u>
<u>L35</u> l1 or (memory adj2 controller)	78482	<u>L35</u>
<u>L34</u> l4 same (ASIC or FPGA)	5886	<u>L34</u>
<u>L33</u> l31 and l4	10	<u>L33</u>
<u>L32</u> l31 same l4	2	<u>L32</u>
<u>L31</u> signal adj4 (indicate or indicating) adj4 valid adj4 invalid	72	<u>L31</u>
<u>L30</u> signal adj4 (indicate or indicating) adj4 valid adj4 invalid	0	<u>L30</u>
<u>L29</u> L27 and ((memory or bank) adj2 controller)	11	<u>L29</u>
<u>L28</u> L27 same ((memory or bank) adj2 controller)	1	<u>L28</u>
<u>L27</u> l4 adj2 l24	58	<u>L27</u>
<u>L26</u> L25 same ((memory or bank) adj2 controller)	79	<u>L26</u>
<u>L25</u> L24 same l4	1768	<u>L25</u>
<u>L24</u> enable adj signal	71514	<u>L24</u>
<u>L23</u> (indicate or indicating or determine or determining) same l4 same (erroneous or incorrect or unstable)	205	<u>L23</u>

<u>L22</u>	(indicate or indicating or determine or determining) adj5 l4 adj5 (erroneous or incorrect or unstable)	0	<u>L22</u>
<u>L21</u>	l4 adj6 (malfunction or erroneous)	61	<u>L21</u>
<u>L20</u>	l4 adj5 (erroneous adj3 (state or output))	4	<u>L20</u>
<u>L19</u>	l4 adj5 (invalid adj3 (state or output))	39	<u>L19</u>
<u>L18</u>	(determining or determine) adj5 l4 adj5 (valid adj2 (state or output))	5	<u>L18</u>
<u>L17</u>	(determining or determine) same l4 same (valid adj2 (state or output))	110	<u>L17</u>
<u>L16</u>	l14 same (erroneous or unstable or glitch)	0	<u>L16</u>
<u>L15</u>	l14 and l11	0	<u>L15</u>
<u>L14</u>	enable adj4 output adj4 l4	68	<u>L14</u>
<u>L13</u>	l12 and l11	1	<u>L13</u>
<u>L12</u>	(enable or valid or validate) adj5 (output or state) adj5 l4	205	<u>L12</u>
<u>L11</u>	l4 adj5 (erroneous or unstable)	24	<u>L11</u>
<u>L10</u>	l4 adj5 (error or erroneous or unstable)	819	<u>L10</u>
<u>L9</u>	l4 adj5(indicate or indicating) adj5 (output or state) adj5 valid	1	<u>L9</u>
<u>L8</u>	l7 same (error or erroneous or unstable)	192	<u>L8</u>
<u>L7</u>	l4 same (indicate or indicating) same (output or state) same valid	1194	<u>L7</u>
<u>L6</u>	l4 same (l2 or l3) same l1	5	<u>L6</u>
<u>L5</u>	L4 same l3 same l2 same l1	0	<u>L5</u>
<u>L4</u>	(state generator) or (state machine)	47345	<u>L4</u>
<u>L3</u>	synchronous adj2 memory	4778	<u>L3</u>
<u>L2</u>	SDRAM	20973	<u>L2</u>
<u>L1</u>	bank adj2 controller	1364	<u>L1</u>

END OF SEARCH HISTORY